

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2578	software near4 radio	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2006/02/03 10:53
L2	738	1 and ((digital adj signal adj process\$4) or dsp)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/02/03 10:53
L3	347	2 and (mux\$4 or multiplex\$4)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/02/03 10:54
L4	248	3 and ((add or subtract or multiply or multiplication) or (arithmetic))	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/02/03 10:54
L5	144	4 and (cdma or wcdma or "w-cdma" or quadrature or "in-phase")	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/02/03 10:55
L6	17	5 and 370/328-339.ccls.	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/02/03 10:55

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	filho-eliseu\$.in.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2006/02/03 09:21

Day : Friday
Date: 2/3/2006

Time: 09:22:02



PALM INTRANET

Application Number Information

Application Number: **10/712288**

Assignments

Filing or 371(c) Date: **11/12/2003**

Effective Date: **11/12/2003**

Application Received: **11/14/2003**

Pat. Num./Pub. Num: **/20050102340**

Issue Date: **00/00/0000**

Date of Abandonment: **00/00/0000**

Attorney Docket Number: **M-15245 US**

Examiner Number: **74789 / DUONG, FRANK**

Group Art Unit: **2666**

IFW IMAGE

Class/Subclass:

370/535.000

Lost Case: **NO**

Interference Number:

Unmatched Petition: **NO**

L&R Code: Secrecy Code:1

Third Level Review: **NO**

Secrecy Order: **NO**

Status: **30 /DOCKETED NEW CASE - READY FOR EXAMINATION**

Status Date:
05/05/2005

Confirmation Number: **9342**

Oral Hearing: **NO**

Title of Invention: **APPARATUS FOR SPREADING, SCRAMBLING AND CORRELATION IN A RECONFIGURABLE DIGITAL SIGNAL PROCESSOR**

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
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Patent Assignment Abstract of Title

Total Assignments: 3

Application #: 10712288 **Filing Dt:** 11/12/2003

Patent #: NONE

Issue Dt:

PCT #: NONE

Publication #: US20050102340

Pub Dt: 05/12/2005

Inventor: Eliseu M. Chaves Filho

Title: Apparatus for spreading, scrambling and correlation in a reconfigurable digital signal processor

Assignment: 1

Reel/Frame:	<u>014707 / 0187</u>	Received:	11/21/2003	Recorded:	11/12/2003	Mailed:	06/09/2004	Pages:	2
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Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: FILHO, ELISEU M. CHAVES

Exec Dt: 11/07/2003

Assignee: MORPHO TECHNOLOGIES,
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Correspondent: MACPHERSON, KWOK , CHEN ET.AL.
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Assignment: 2

Reel/Frame:	<u>016863 / 0843</u>	Received:	08/11/2005	Recorded:	08/09/2005	Mailed:	12/07/2005	Pages:	13
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Conveyance: RELEASE OF SECURITY AGREEMENT

Assignors: SMART TECHNOLOGY VENTURES III SBIC, L.P.

Exec Dt: 06/08/2004

BRIDGE WEST, LLC

Exec Dt: 06/08/2004

ELLUMINA, LLC

Exec Dt: 06/08/2004

AMIRRA INVESTMENTS LTD.

Exec Dt: 06/08/2004

AMIR MOUSSAVIAN

Exec Dt: 06/08/2004

MILAN INVESTMENTS, LP

Exec Dt: 06/08/2004

LIBERTEL, LLC

Exec Dt: 06/08/2004

Assignee: MORPHO TECHNOLOGIES, INC.

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Correspondent: CHRISTIAN KUHLEN ESQ.
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Assignment: 3

Reel/Frame:	<u>015550 / 0970</u>	Received:	07/14/2004	Recorded:	07/09/2004	Mailed:	01/12/2005	Pages:	12
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Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: MORPHO TECHNOLOGIES

Exec Dt: 06/15/2004

Assignees: SMART TECHNOLOGY VENTURES III SBIC, L.P.

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Search Results as of: 2/3/2006 9:22:10 A.M.



Continuity Information for 10/712288

Parent Data
No Parent Data

Child Data
PCT/US04/37677 is a continuation of 10712288

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Date: 2/3/2006

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Time: 09:22:31

Application Number Information

Application Number: **PCT/US04/37677**

Assignments

Filing or 371(c) Date: **11/10/2004**

Effective Date: **00/00/0000**

Application Received:

Patent Number:

Issue Date: **00/00/0000**

Date of Abandonment: **00/00/0000**

Attorney Docket Number: **M-15245WO**

Status: **566 /PCT - INTERNATIONAL SEARCH REPORT MAILED TO IB**

Confirmation Number: **7437**

Title of Invention: **APPARATUS FOR SPREADING, SCRAMBLING AND CORRELATION IN A RECONFIGURABLE DIGITAL SIGNAL PROCESSOR**

Examiner Number: **74789 / DUONG, FRANK**

Group Art Unit: **2666**

Class/Subclass:
370/535.000

Lost Case: **NO**

Interference Number:

Unmatched Petition: **NO**

L&R Code: Secrecy Code:1

Third Level Review: **NO**

Secrecy Order: **NO**

Status Date:
11/10/2005

Oral Hearing: **NO**

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
<u>50437677</u>	<u>50C0</u>	11/14/2005	No Charge to Location	No Charge to Name	<u>AGUIRRE,CIRO</u>	FRAN/01/
<u>51437677</u>	<u>50C0</u>	12/23/2004	No Charge to Location	No Charge to Name	TTRAN9	FRAN/01/

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Time: 09:22:41

 **PALM INTRANET**

Inventor Information for 10/712288

Inventor Name	City	State/Country
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Day : Friday
Date: 2/3/2006

Time: 09:22:49



PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = CHAVES FILHO

First Name = ELISEU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10712288	Not Issued	30	11/12/2003	Apparatus for spreading, scrambling and correlation in a reconfigurable digital signal processor	CHAVES FILHO, ELISEU M.

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name
<input type="text" value="CHAVES FILHO"/>	<input type="text" value="ELISEU"/>

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